

Amendments to the Specification:

Please replace the paragraph at lines 19 – 21 on page 13 with the following amended paragraph:

Fig. 11A is a schematic diagram of a Reduced Instruction Set Computer (RISC) neuron according to an embodiment of the invention;

Please replace the paragraph at lines 22 – 26 on page 20 with the following amended paragraph:

Further illustrations of a neuron 100, are provided by Fig. 11B, and Fig. 11C. Figure 11B is a schematic diagram of the neuron, and ~~Fig. 3~~ Fig. 11C illustrates the layout of the neuron shown schematically in Fig. 11B when implemented as a RISC processor.